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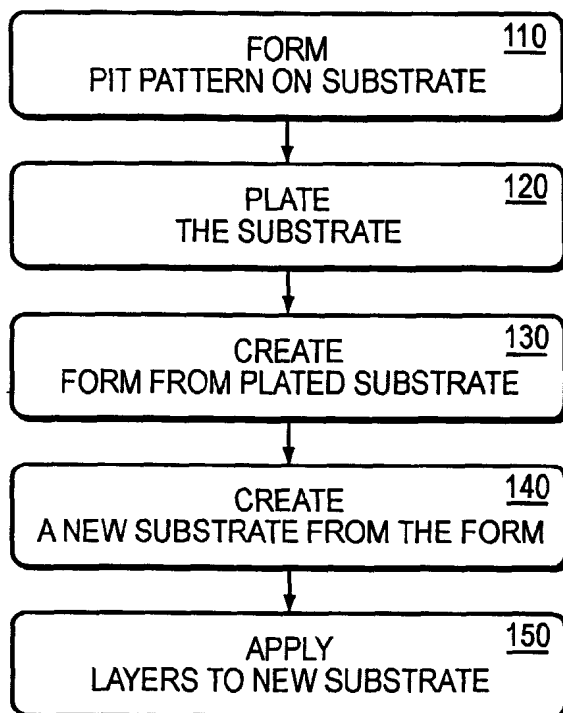
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(54) Title: FABRICATION OF SEMICONDUCTOR DEVICES



(57) Abstract: Fabrication of microelectronic devices is ac-
complished using a substrate having a recessed pattern. In
one approach, a master form is used to replicate a substrate
having a pit pattern. In another approach, the substrate is
produced by etching. A series of stacked layers having de-
sired electrical characteristics is applied to the substrate and
planarized in a manner that creates electrical devices and
connections therebetween. The microelectronic devices can
include a series of row and columns and are used to store
data at their intersection.

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A. CLASSIFICATION OF SUBJECT MATTER
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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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EPO-Internal, WPI Data, PAJ, INSPEC, COMPENDEX

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 2000, no. 02, 29 February 2000 (2000-02-29) -& JP 11 305055 A (SHARP CORP), 5 November 1999 (1999-11-05) abstract; figure 1 ----	1-4, 26
X	US 6 117 720 A (HARSHFIELD STEVEN T) 12 September 2000 (2000-09-12) figures 1-8 column 3, line 29 -column 5, line 21 ----	12-25, 27, 28
X	PATENT ABSTRACTS OF JAPAN vol. 1995, no. 06, 31 July 1995 (1995-07-31) -& JP 07 066438 A (TONEN CORP), 10 March 1995 (1995-03-10) abstract; figures 1-4 ----- -/--	1-3, 11, 21, 26

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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